



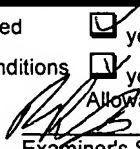
UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3521

SERIAL NUMBER 10/518,157	FILING OR 371(c) DATE 12/10/2004 RULE	CLASS 257	GROUP ART UNIT 2822	ATTORNEY DOCKET NO. 18497	
APPLICANTS Takashi Nakano, Tokyo, JAPAN; Hirokazu Tohya, Tokyo, JAPAN; ** CONTINUING DATA ***** This application is a 371 of PCT/JP03/07397 06/11/2003 ** FOREIGN APPLICATIONS ***** JAPAN 2002-170840 06/12/2002					
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged  Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 23	INDEPENDENT CLAIMS 2
ADDRESS 23389					
TITLE Semiconductor device, semiconductor circuit and method for producing semiconductor device					
FILING FEE RECEIVED 1100	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		